

## Memory Module Specification

## KVR533D2S4/1G 1GB 128M x 64-Bit DDR2-533 CL4 200-Pin SODIMM

## **DESCRIPTION:**

This document describes ValueRAM's 128M x 64-bit 1GB (1024MB) CL4 DDR2-533 SDRAM (Synchronous **DRAM**) memory module. The components on this module include sixteen 64M x 8-bit (16M x 8-bit x 4 Bank) DDR2-533 SDRAM in FBGA packages. This 200-pin SODIMM uses gold contact fingers and requires +1.8V. The electrical and mechanical specifications are as follows:

## FEATURES:

- $\checkmark$  Clock Cycle Time (tCK) CL=4
- $\checkmark$  Row Cycle Time (tRC)
- ☑ Refresh to Active/Refresh Command Time (tRFC)
- $\checkmark$  Row Active Time (tRAS)
- Single Power Supply of
- Dever 2
- **UL** Rating

3.75ns (min.) / 8ns (max.) 55ns (min.) 75ns 40ns (min.) / 70,000ns (max.) +1.8V (+/- .1V) 2.440 W (operating) 94 V - 0

**Note:** The module defined in this data sheet is one of several configurations available under this part number. While all configurations are compatible, the DRAM combination and/or the module height may vary from what is described here. However, in no configuration will the module height exceed 30.00mm.



